

TTP Communication Protocol**RATIONALE**

TTP is used in a variety of aerospace applications (for example, Boeing 787 power generation systems and environmental controls, cabin pressure systems for Airbus A380, Aermacchi M-346 FADEC...) and continues to attract significant cross-industry attention for commercial and defense applications.

The SAE standardization of TTP:

- acts as an integration risk reduction mechanism
- ensures compatible physical implementations
- enables common test/maintenance equipment
- leverages industry investments
- ensures openness and enables multiple component and tool suppliers

and therefore reduces the overall cost and risk of applying this technology.

The SAE standardization based on TTP specification protects long-term system design investments, enables development of the COTS ecosystem, and minimizes sourcing risks for OEMs, integrators and system suppliers.

INTRODUCTION

The Time-Triggered Protocol (TTP) is a real-time communication protocol for the interconnection of electronic modules of distributed fault-tolerant real-time systems. TTP is a core technology for fault-tolerant distributed embedded computing and enables design of deterministic embedded computing platforms for critical systems. As such, it contains communication protocol capability for data exchange among nodes, but also provides higher level services for design of reusable generic platforms with robust partitioning among functions. This document specifies the structure of the TTP protocol, services and mechanisms on an abstract level without implementation-specific details.

TTP's features support the design of systems with a high degree of dependability, safety, availability, reliability, maintainability, and reduced system complexity.

Some methods contained in the document are protected by granted and pending patents, and underlie SAE standard IP policy.

is published by SAE to advance the state of technical and engineering sciences. The use of this report is suitability for any particular use, including any patent infringement arising therefrom, is the sole responsibility of the user."

SAE reviews each technical report at least every five years at which time it may be reaffirmed, revised, or cancelled. SAE inv

Copyright © 2011 SAE International

All rights reserved. No part of this public

thout the prior wri

I system or transmitted, in any form or

TO PLACE A DOCUMENT ORDER: Tel: 877-606-7323 (inside USA and Canada)
Tel: +1 724-776-4970 (outside USA)
Fax: 724-776-0790
Email: CustomerService@sae.org
[sae.org](http://www.sae.org)

**SAE values your input. To provide feedback
on this Technical Report, please visit
<http://www.sae.org/technical/standards/AS6003>**

SAE WEB ADDRESS:

TABLE OF CONTENTS

1.	SCOPE.....	4
1.1	Purpose.....	4
1.2	Application.....	4
1.3	Interpretation.....	4
2.	REFERENCES.....	4
2.1	SAE Publications.....	4
2.2	Other Publications.....	5
2.3	Structure of the Document.....	5
2.4	Typographic Conventions	5
3.	OVERVIEW OF A TTP-BASED SYSTEM	6
3.1	Structure of a TTP Network	6
3.2	Structure of an Electronic Module (Node).....	6
3.2.1	Communication Layer	7
3.2.1.1	Central Characteristics of the Time-Triggered Protocol	7
3.2.1.1.1	Protocol Services	7
3.2.1.1.2	Autonomous Operation of the TTP Controller	8
3.2.1.1.3	Global Time Base.....	8
3.2.1.1.4	TDMA Bus Access	8
3.2.1.1.5	Configuration Data	9
3.2.1.2	Overview of the Host Interface.....	9
3.2.1.3	Overview of the Physical Layer Interface	9
3.2.2	Application Layer - Time-Triggered Architecture	9
3.2.2.1	Application Layer Synchronous to Communication Layer.....	9
3.2.2.2	Application Layer Asynchronous to Communication Layer	9
4.	HOST INTERFACE.....	10
5.	DATA LINK LAYER.....	11
5.1	TDMA Scheme.....	11
5.2	TTP Frame Layout	12
5.2.1	Frame Header	12
5.2.1.1	Frame Type	13
5.2.1.2	Mode Change Request	13
5.2.2	C-State	13
5.2.3	Frame CRC	14
5.3	Frame Types	15
5.3.1	N-Frame	15
5.3.2	I-Frame.....	16
5.3.3	Cold Start Frame	17
5.3.4	X-Frame	17
5.4	Frame Status.....	18
5.4.1	Null-Frame Reception Status	20
5.4.2	Valid Frame Reception Status	20
5.4.3	Invalid Frame Reception Status	20
5.4.4	Incorrect Frame Reception Status	20
5.4.5	Tentative Frame Reception Status	20
5.4.6	Correct Frame Reception Status	20
5.4.7	Cluster Mode Violation	20
5.4.8	Slot Status.....	20
6.	PHYSICAL LAYER – INTERFACE.....	21

7.	FAULT-TOLERANT DISTRIBUTED CLOCK IN TTP	22
7.1	Timing Parameters.....	22
7.1.1	Microtick	23
7.1.2	Macrotick	23
7.1.3	Precision	23
7.2	Synchronization.....	24
7.2.1	Action Time and Exchange of Timing Information in a TTP Network.....	24
7.2.2	Calculation of the Correction Term	26
7.2.3	Correcting the Local Clock.....	27
8.	PROTOCOL SERVICES.....	28
8.1	Startup.....	28
8.1.1	Integration	28
8.1.2	Cold-start.....	29
8.1.2.1	Timeouts	30
8.1.2.1.1	Startup Timeout.....	30
8.1.2.1.2	Listen Timeout.....	30
8.1.2.1.3	Cold Start Timeout	30
8.1.2.2	Big Bang.....	31
8.2	Membership	32
8.2.1	Membership Vector.....	33
8.2.2	Multiplexed Slots.....	33
8.3	Acknowledgment.....	34
8.3.1	Acknowledgement Algorithm	34
8.3.2	Acknowledgement Algorithm – State 1	34
8.3.3	Acknowledgement Algorithm – State 2	35
8.3.4	Sequence of Checks	36
8.4	Clique Detection.....	37
8.4.1	Clique Error	37
8.4.2	Communication System Blackout	37
8.5	Host/Controller Life-sign	37
8.6	Cluster Modes	38
8.6.1	Distribution Phase	38
8.6.2	Acceptance Phase	38
8.6.3	Execution Phase	39
9.	PROTOCOL STATES	39
9.1	Protocol States and State Transitions	39
9.1.1	TTP Controller – Protocol State Overview	39
9.1.2	TTP controller – Protocol State Transitions	40
9.2	Operation in TTP Protocol States	42
9.2.1	Protocol Variables	42
9.2.2	Freeze State.....	43
9.2.3	Init State	44
9.2.4	Listen State	44
9.2.5	Cold Start State	46
9.2.6	Active and Passive State	47
9.2.6.1	Pre-send Phase	47
9.2.6.2	Transmission Phase	50
9.2.6.3	Post-receive Phase	50
9.2.6.4	Idle Phase	51
APPENDIX A	GLOSSARY ACRONYMS	52
APPENDIX B	COMPATIBILITY REQUIREMENTS FOR EXISTING TTP CONTROLLER IMPLEMENTATION	53